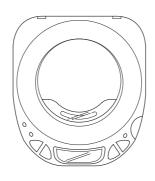


XP-V310 Y1B(LL) Y1B(S) Y1(S) Y1B(D) Y1B(LT)

XP-V311 AHRJ1(S) AHRJ1(D) AEZ1(L) AHAB(S) AEZ1(S)

XP-V312 AK1(S) AEZ1(S)



SERVICE MANUAL

COMPACT DISC PLAYER

BASIC CD MECHANISM: DA23L

This Service Manual is the "Revision Publishing" and replaces "Simple Manual" (S/M Code No. 09-003-339-8T2).





PROTECTION OF EYES FROM LASER BEAM DURING SERVICING

This set employs laser. Therefore, be sure to follow carefully the instructions below when servicing.

WARNING!

WHEN SERVICING, DO NOT APPROACH THE LASER EXIT WITH THE EYE TOO CLOSELY. IN CASE IT IS NECESSARY TO CONFIRM LASER BEAM EMISSION. BE SURE TO OBSERVE FROM A DISTANCE OF MORE THAN 30cm FROM THE SURFACE OF THE OBJECTIVE LENS ON THE OPTICAL PICK-UP BLOCK.



- Caution: Invisible laser radiation when open and interlocks defeated avoid exposure to beam.
- Advarsel: Usynling laserståling ved åbning, når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

VAROITUS!

Laiteen Käyttäminen muulla kuin tässä käyttöohjeessa mainitulla tavalla saattaa altistaa käyt-täjän turvallisuusluokan 1 ylittävälle näkymättömälle lasersäteilylle.

VARNING!

Om apparaten används på annat sätt än vad som specificeras i denna bruksanvising, kan användaren utsättas för osynling laserstrålning, som överskrider gränsen för laserklass 1.

Precaution to replace Optical block (SF-P200)

Body or clothes electrostatic potential could ruin laser diode in the optical block. Be sure ground body and workbench, and use care the clothes do not touch the diode.

1) After the connection, remove solder shown in the right figure.

CAUTION

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

ATTENTION

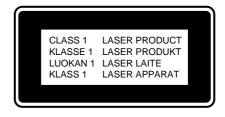
L'utilisation de commandes, réglages ou procédures autres que ceux spécifiés peut entraîner une dangereuse exposition aux radiations.

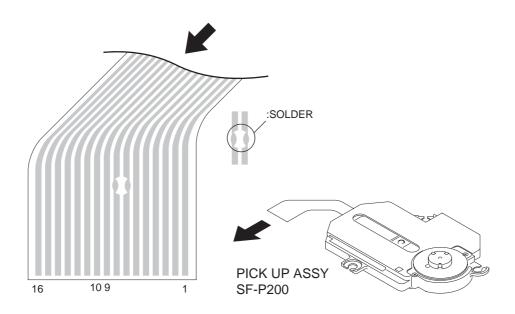
ADVARSEL!

Usynlig laserståling ved åbning, når sikkerhedsafbrydereer ude af funktion. Undgå udsættelse for stråling.

This Compact Disc player is classified as a CLASS 1 LASER product.

The CLASS 1 LASER PRODUCT label is located on the rear exterior.





SPECIFICATIONS

Tracking system Laser pickup D/A conversion 3-beam laser

Semiconductor laser

8-times oversampling digital filter + 1-bit DAC

Frequency response Output Maximum output

Power supply

8-times oversampling digital filter + 1-bit DAC
20 - 20,000 Hz
PHONES/LINE OUT jack (stereo mini-jack)
12 mW + 12 mW (EIAJ 16 ohms at 1 kHz)
500 mV (47 k ohms at 1 kHz)
DC 3 V using two LR6 (size AA) alkaline batteries
DC 2.4 V using two commercially available
rechargeable batteries (Ni-Cd 1.2 V 700 mAh)
AC house current using the supplied AC adaptor AC house current using the supplied AC adaptor

Maximum outside dimensions

128 (W) × 28 (H) × 144.5 (D) mm

 $(5.1/6 \times 1.1/6 \times 5.3/4 \text{ in.})$ (excluding projecting parts

and controls)

Weight Approx. 220g (7.7 oz.) excluding batteries

• Design and specifications are subject to change without notice.

ELECTRICAL MAIN PARTS LIST

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO	PART NO.	KANRI DESCRIPTION NO.	REF. NO		ANRI DESCRIPTION NO.
IC	87-A21-448-04 87-A21-083-04 87-A21-381-04 87-A21-591-03	40 C-IC,BH6508FS 40 C-IC,LA9235M	C354 C355 C356 C357 C358	87-010-196-080 87-010-312-080 87-010-312-080 87-010-196-080 87-010-322-080	CHIP CAPACITOR,0.1-25 C-CAP,S 15P-50 CH C-CAP,S 15P-50 CH CHIP CAPACITOR,0.1-25 C-CAP,S 100P-50 CH
	87-A21-085-04 8A-HC7-607-01	40 C-IC,TA2120FN	C359 C360 C361 C362 C363	87-A10-369-080 87-016-669-080 87-010-322-080 87-016-669-080 87-010-197-080	C-CAP,S 0.47-16 K B C-CAP,S 0.1-25 K B C-CAP,S 100P-50 CH C-CAP,S 0.1-25 K B CAP, CHIP 0.01 DM
TRANSISTO	89-211-323-08 87-A30-332-04 87-A30-278-04 87-A30-287-04 87-A30-246-04	40 C-TR,CPH3106 40 C-FET,2SK2980 40 C-TR,DTC114TKA	C364 C365 C367 C368 C701	87-016-369-080 87-010-322-080 87-010-175-080 87-010-196-080 87-010-501-040	C-CAP,S 0.033-25 B K C-CAP,S 100P-50 CH CAP 560P CHIP CAPACITOR,0.1-25 E/CAP GAS 47-4
	86-NFZ-667-04 89-416-643-08 89-324-123-08	40 C-TR,DTC123JKA 80 C-TR,2SD1664R	C702 C703 C704 C705 C706	87-010-495-040 87-010-498-040 87-010-503-040 87-010-503-040 87-010-498-040	CAP,E 2.2-50 GAS CAP,E 10-16 GAS CAP,E 220-4 GAS CAP,E 220-4 GAS CAP,E 10-16 GAS
DIODE	87-A40-592-04 87-A40-590-04 87-A40-554-04	40 C-DIODE,HRW0202A 40 C-DIODE,RB491D	C707 C708 C709 C710 C711	87-010-501-040 87-A10-826-080 87-A10-826-080 87-012-155-080 87-012-155-080	E/CAP GAS 47-4 C-CAP,S 1-10 K B C-CAP,S 1-10 K B C-CAP 180P-50CH C-CAP 180P-50CH
MAIN C.B	87-A40-469-08 87-A40-836-04		C712 C713 C714 C715 C716	87-012-141-080 87-010-196-080 87-010-196-080 87-010-196-080 87-010-196-080	CHIP-CAPACITOR, 0.22-16F CHIP CAPACITOR, 0.1-25 CHIP CAPACITOR, 0.1-25 CHIP CAPACITOR, 0.1-25 CHIP CAPACITOR, 0.1-25
C101 C102 C103 C104 C105	87-010-553-04 87-010-551-04 87-A10-505-04 87-010-503-04 87-010-498-04	40 CAP,E 33-10 GAS 40 CAP,E 220-6.3 105 SF 40 CAP,E 220-4 GAS	C717 C801 C802 C803 C804	87-010-196-080 87-010-501-040 87-010-196-080 87-010-196-080 87-010-178-080	CHIP CAPACITOR, 0.1-25 E/CAP GAS 47-4 CHIP CAPACITOR, 0.1-25 CHIP CAPACITOR, 0.1-25 CHIP CAP 1000P
C106 C107 C108 C109 C110	87-010-502-04 87-010-196-08 87-012-145-08 87-010-198-08 87-A10-826-08	80 CHIP CAPACITOR,0.1-25 80 CAP, CHIP S 270P CH 80 CAP, CHIP 0.022	C805 C806 C807 C808 CN201	87-010-322-080 87-010-319-080 87-010-319-080 87-010-196-080 87-A61-104-010	C-CAP,S 100P-50 CH C-CAP,S 56P-50 CH C-CAP,S 56P-50 CH CHIP CAPACITOR,0.1-25 CONN,16P H WHITE 52089-1610
C111 C112 C113 C115 C116	87-010-196-08 87-010-196-08 87-A10-826-08 87-010-196-08 87-010-196-08	80 CHIP CAPACITOR,0.1-25 80 C-CAP,S 1-10 K B 80 CHIP CAPACITOR,0.1-25	CN202 FB701 FB702 FB703 FB704	87-009-411-010 87-A50-623-080 87-A50-623-080 87-A50-623-080 87-A50-623-080	CONN,6P ZH V C-COIL,BK2125HS102 C-COIL,BK2125HS102 C-COIL,BK2125HS102 C-COIL,BK2125HS102
C117 C201 C202 C204 C205	87-010-196-08 87-A10-505-04 87-010-175-08 87-010-213-08 87-010-213-08	40 CAP,E 220-6.3 105 SF 80 CAP 560P 80 C-CAP,S 0.015-50 B	J101 J701 L101 L102 L301	87-A60-421-010 85-HC5-616-010 87-A50-574-010 87-A50-573-010 87-A50-455-080	JACK,DC HEC3600 BLK 6 JACK,3.5 ST W/R GRN COIL,100UH #7607 COIL,330UH LHL06NB C-COIL,47UH-FSLB2520
C206 C207 C208 C209 C210	87-A10-826-08 87-A10-826-08 87-010-177-08 87-010-213-08 87-010-213-08	80 C-CAP,S 1-10 K B 80 C-CAP,S 820P-50 SL 80 C-CAP,S 0.015-50 B	L302 L351 L801 L802 LCD101	87-A50-501-080 87-A50-501-080 87-A50-501-080 87-A50-455-080 8A-HC7-602-010	C-COIL,10UH-FSLB2520 C-COIL,10UH-FSLB2520 C-COIL,10UH-FSLB2520 C-COIL,47UH-FSLB2520 LCD,AHC-7
C212 C301 C302 C303 C304	87-A10-826-08 87-016-557-04 87-010-502-04 87-016-557-04 87-010-502-04	40 CAP,E 100-6.3 SF 40 CAP ELECT GAS 100/4 40 CAP,E 100-6.3 SF	R105 R107 R307 R308 R309	87-022-355-080 87-022-358-080 87-022-202-080 87-022-202-080 87-022-202-080	C-RES,S10K-1/10W F C-RES,S 18K-1/10W F C-RES,S33K 1/10WF C-RES,S33K 1/10WF C-RES,S33K 1/10WF
C305 C306 C308 C309 C311	87-010-501-04 87-010-196-08 87-010-196-08 87-010-178-08 87-010-318-08	80 CHIP CAPACITOR,0.1-25 80 CHIP CAPACITOR,0.1-25 80 CHIP CAP 1000P	R310 R311 R312 S101 S102	87-022-202-080 87-022-364-080 87-022-364-080 87-022-364-080 87-022-364-080 87-022-364-080	C-RES,S33K 1/10WF C-RES,S33K 1/10WF C-RES,S 82K-1/10W F C-RES,S 82K-1/10W F SW,TACT EVQ11G04M SW,TACT EVQ11G04M
C313 C314 C351 C352 C353	87-A10-826-08 87-A10-201-08 87-016-557-04 87-010-503-04 87-A10-826-08	80 C-CAP,S0.33-16 KB 40 CAP,E 100-6.3 SF 40 CAP,E 220-4 GAS	\$103 \$104 \$105 \$106 \$801	87-A90-095-080 87-A90-095-080 87-A90-095-080 87-A90-095-080 87-A90-095-080 87-A91-622-010	SW,TACT EVQ11G04M SW,TACT EVQ11G04M SW,TACT EVQ11G04M SW,TACT EVQ11G04M SW,TACT EVQ11G04M SW,MICRO PV1102

REF. NO	PART NO.	KANRI DESCRIPTION NO.
S802	87-A91-742-010	SW,SL 4-1-3 HSW2061-010010
VR701	87-A90-462-010	VR,RTRY 30KCX2 H RK14J12A0
X351	87-470-202-080	C-VIB.CER 16.93MHZ CSACV-MXJO

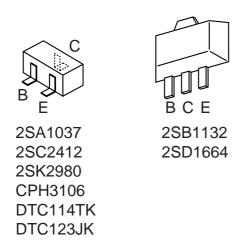
• Regarding connectors, they are not stocked as they are not the initial order items.

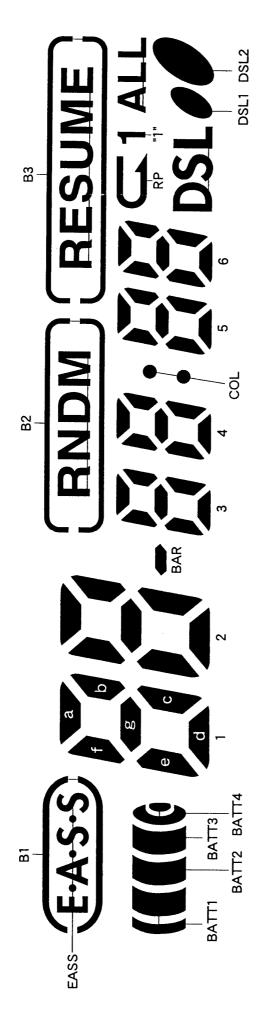
The connectors are available after they are supplied from connector manufacturers upon the order is received.

チップ抵抗 Chip resistor

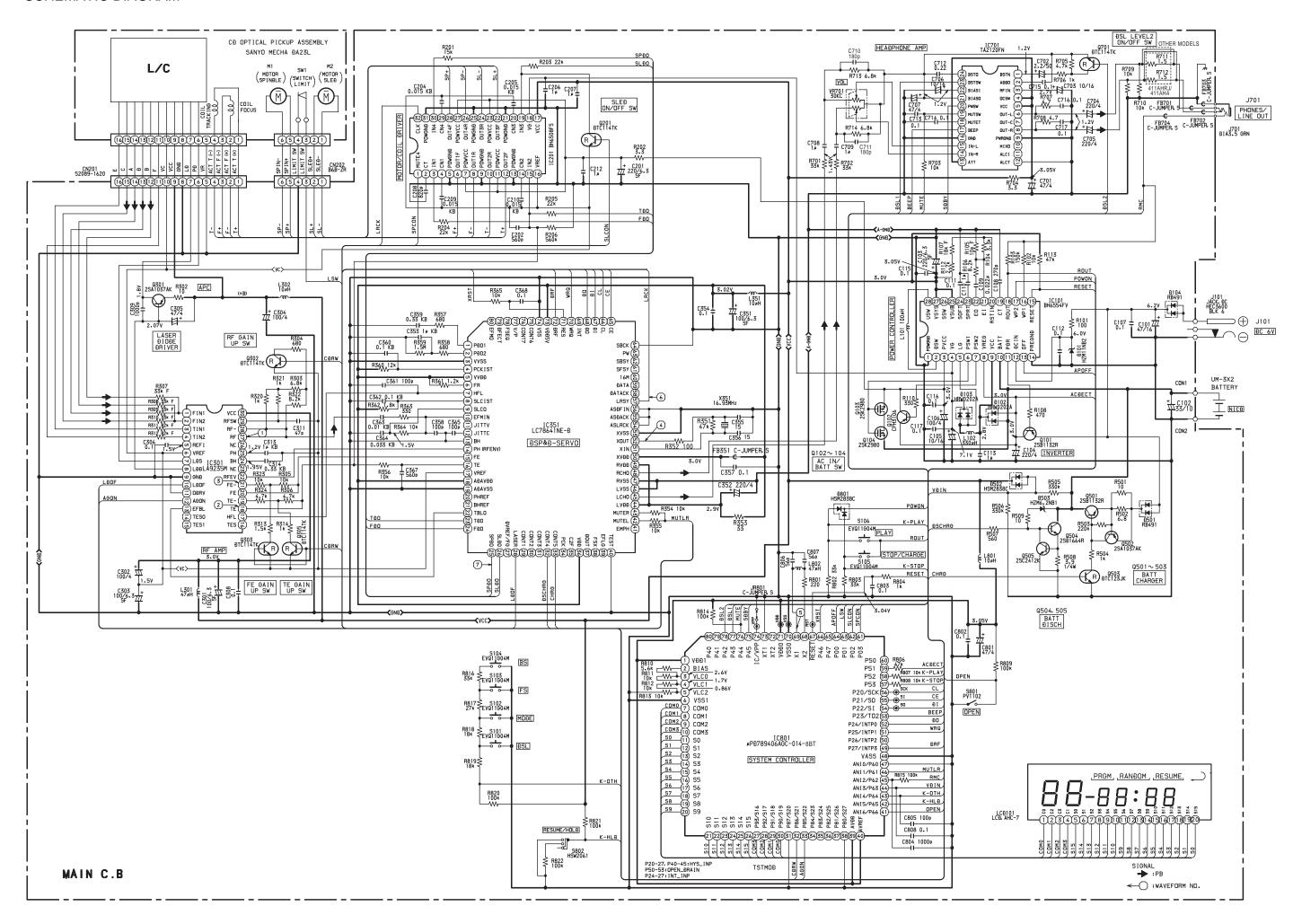
容量	種類	許容誤差	記号	寸法/Dime	ensions	(mm)		抵抗コード : A
Wattage	Type	Tolerance	Symbol	外形/Form	L	W	t	Resistor Code : A
1/16W	1005	± 5%	CJ		1.0	0.5	0.35	104
1/16W	1608	± 5%	CJ	L J t	1.6	0.8	0.45	108
1/10W	2125	± 5%	CJ		2	1.25	0.45	118
1/8W	3216	± 5%	CJ	ľ	3.2	1.6	0.55	128

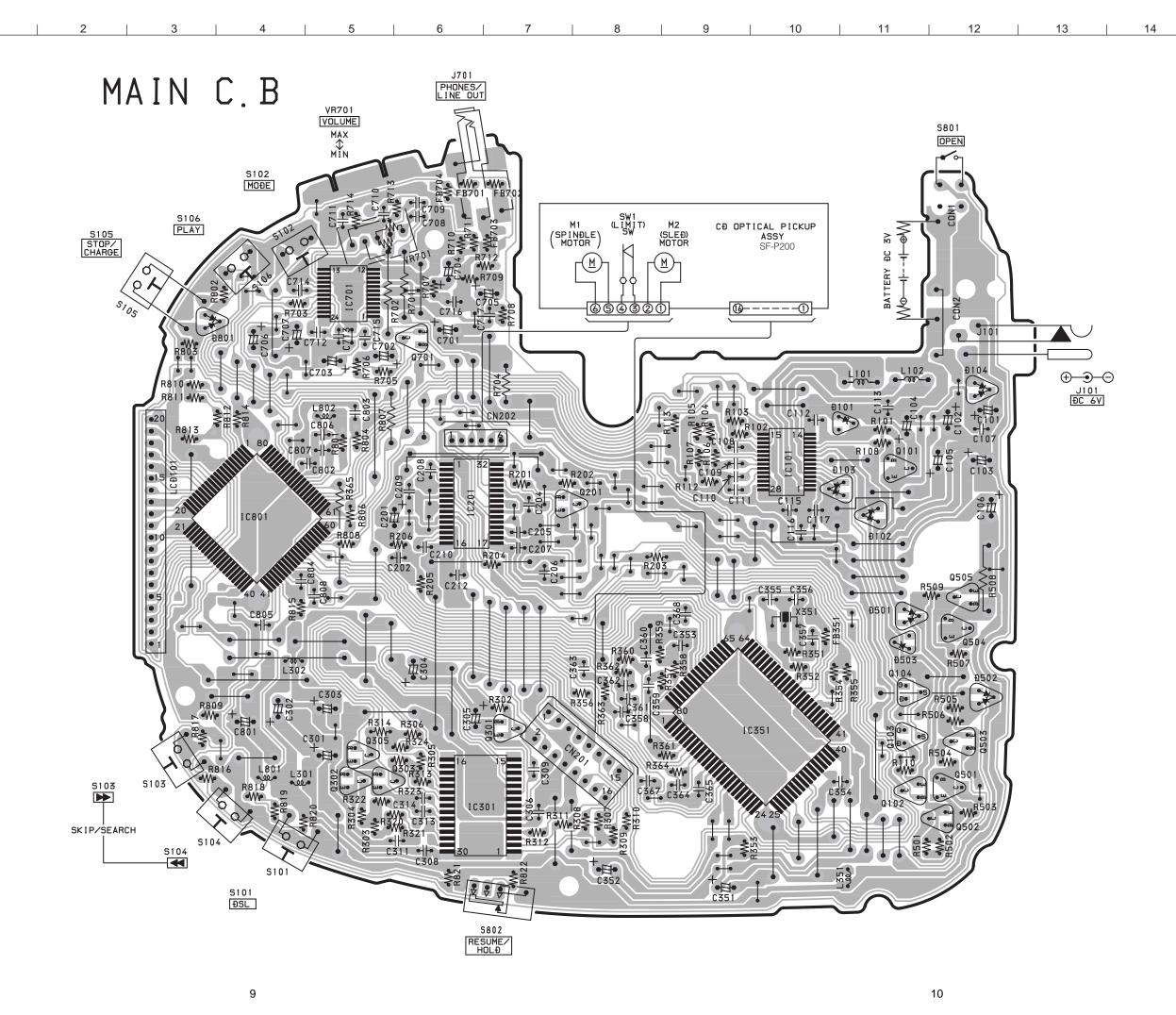
TRANSISTOR ILLUSTRATION

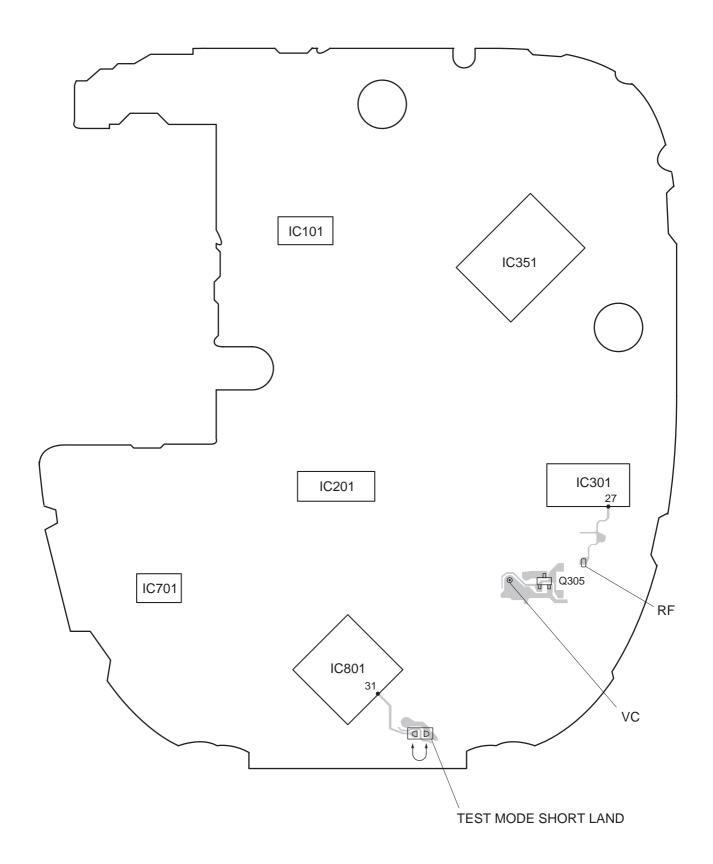




8	-	2	3	4	5	9	7	8	6	10	11	12	13	14	15	16	17	18	19	20
COMO COMO	COMO	-	-	-	—— ВАТТ2	B1	RNDM	1a		2a	BAR	3a		4a	COL	5a	B2	6a	B3	RESUME
COM1] -		COM1	COM1 BATT1 EASS	EASS	11	1b	2f	2b	3f	3b	4£	4b	5f	2p	9ŧ	q9	1	ALL
COM2		COM2			—— ВАТТЗ		1e	1g	2e	2g	Зе	3g	4e	4g	2e	5g	e e	89	RP	DSL2
COM3	-		СОМЗ		BATT4		ρι	၂၁	2d	2c	3d	3c	p4	4c	2d	5c	P9	90	DSL	DSL1







The servo circuit of this model has been designed to be free of adjustments and controlled within the IC. Therefore, adjustments and disk judgement are performed automatically every time the TOC is read out. The adjustment status of each servo inside the IC can be monitored in this test mode.

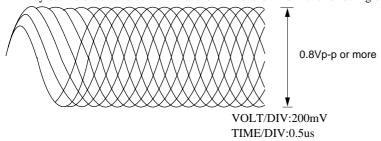
1. Startup procedure

- 1) Short the test land.
- 2) Insert the AC plug.
- 3) Press the STOP button. (The test mode starts.)
- Note 1) The test mode is canceled by disconnecting the AC plug.
- Note 2) The OPEN/CLOSE switch cannot be operated during the test mode.

2. Checking the RF level

Test point: RF & VC (Vref) Test disk: TCD-782

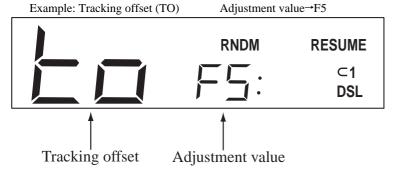
Play back the disk and confirm that the RF waveform is in the following state:



3. Checking each servo

The adjustment values of each servo can be checked by pressing the MODE button repeatedly during playback. The switching procedure is as follows.

Check mode OFF→Vref offset (RO)→focus offset (FO)→tracking offset (TO)→tracking balance (TB)→tracking gain (TG) → focus gain (FG)→focus bias (FB)→check mode OFF



^{*} Adjustment values are indicated in hexadecimal.

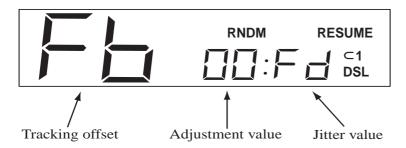
When displaying each mode on the LCD and pressing the PLAY button in the STOP status, the center value is displayed on the LCD. After the disk starts rotating, the adjustment value that was set during automatic adjustment is displayed. The display range of the center values and adjustment values of each mode are as follows. There are 256 steps for displaying the values of all modes.

Center value	Center value	Display range
1) Vref offset (RO)	00	80-7F
2) Focus offset (FO)	00	80-7F
3) Tracking offset (TO)	00	80-7F
4) Tracking balance (TB)	80	00-FF
5) Tracking gain (TG)	40	00-FF
6) Focus gain (FG)	40	00-FF
7) Focus bias (FB)	00	80-7F

4. Amount of change of jitter

The amount of change of jitter is displayed in the focus bias check mode. The displayed value has 256 steps from 00 to FF.

Example: focus bias (FB) Adjustment value→00 Jitter value→FD

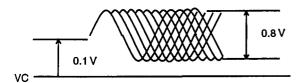


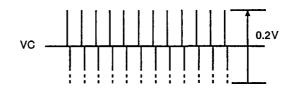
^{*} Adjustment values and jitter values are indicated in hexadecimal.

WAVE FORM

1 IC301 Pin ② RF

VOLT/DIV: 0.2V TIME/DIV: 0.5μS V) IC601 Pin (25) VOLT/DIV: 0.1V SPDO TIME/DIV: 5mS





2 IC301 Pin ® TE

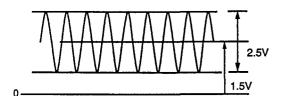
VOLT/DIV: 0.2VTIME/DIV: $50\mu S$

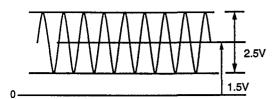


(3) IC301 Pin (2) VOLT/DIV: 0.1V FE- TIME/DIV: 2mS

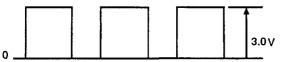


4 IC351 Pin ⊚ VOLT/DIV: 1V XOUT TIME/DIV: 50mS f=16.93MHz



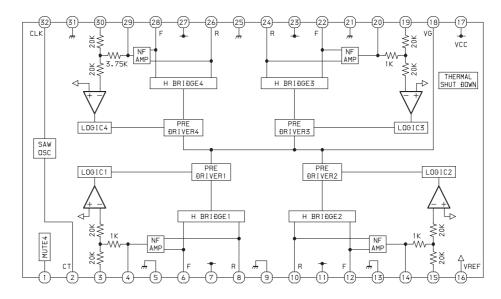


 $\begin{array}{cccc} \textbf{6} & \text{IC351 Pin } \textcircled{57} & & \text{VOLT/DIV: } 2V \\ & \text{LRSY} & & \text{TIME/DIV: } 5 \mu S \\ & & \text{f=44.1kHz} \end{array}$

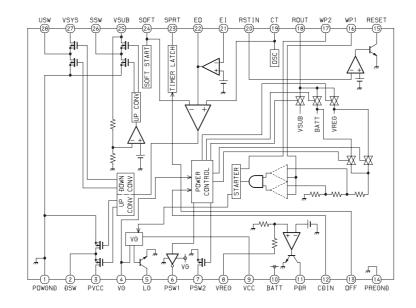


IC BLOCK DIAGRAM

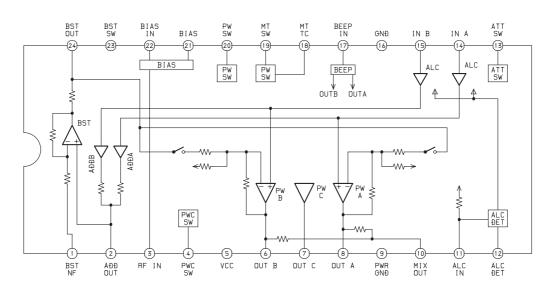
IC, BH6508FS



IC, BH6554FV



IC, TA2120FN



IC DESCRIPTION IC, LC78641NE-D

Pin No.	Pin Name	I/O	Description
1	PDO1	О	Internal VCD control phase comparator output pin.
2	PDO2	О	Internal VCD control phase comparator output pin. OFF for rough servo, ON for phas servo.
3	VVSS	_	Internal VCD ground pin.
4	PCKIST	I	PDO output current adjustment resistor connection pin. (pull up)
_			Internal VCD power supply pin. (2000pF or more path controller to be inserted at a
5	VVDD	-	point nearer to the pin between this pin and GND)
6	FR	I	VCD frequency range adjustment resistor connection pin. (pull up)
7	HFL	I	Mirror detection signal input pin.
8	SLCIST	I	SLCO output current adjustment resistor connection pin. (pull up)
9	SLCO	О	Control outout.
10	EFMIN	I	EFM signal input pin.
11	JITTV	0	Jitter detection monitor pin.
12	JITTC	О	Jitter detection adjustment pin.
13	ВН	I	BH signal input pin. A/D input. (Must be connected to OV when unused)
14	PH (RFENV)	I	PH signal or RFENV signal input pin. A/D input.
15	FE	I	FE signal input pin. A/D input.
16	TE	I	TE signal input pin. A/D input.
17	VREF	I	VREF input pin. A/D input.
			Servo A/D, D/A power supply pin. (2000pF or more path controller to be inserted at a
18	ADAVDD		point nearer to the pin between this pin and GND)
19	ADAVSS	_	Servo A/D, D/A ground pin.
20	PHREF	0	PH reference output pin. D/A output.
21	BHREF	0	BH reference output pin. D/A output.
22	TBLO	0	Tracking balance output pin. D/A output.
23	TDO	0	Tracking control output pin. D/A output.
24	FDO	0	Focus control output pin. D/A output.
25	SPDO	0	Spindle control output pin. D/A output.
26	SLDO	0	Thread control output pin. D/A output.
27	DVREF/FG	I/O	Output driver VREF output pin. Input FG signal input pin. (Must be connected to OV
			when unused)
28	LASER	О	Laser ON/OFF control pin.
29	CONT1	I/O	General-purpose input/output pin 1.
30	CONT2	I/O	General-purpose input/output pin 2.
31	CONT3	I/O	General-purpose input/output pin 3.
32	CONT4	I/O	General-purpose input/output pin 4.
33	CONT5	I/O	General-purpose input/output pin 5.
34	PCK	О	EFM data playback clock monitor pin. Average 4.3218MHz when the phase is locked
35	C2F	О	C2 flag output pin.
26	VDD		Digital power supply pin. (2000pF or more path controller to be inserted at a point
36	VDD	-	nearer to the pin between this pin and GND)

Pin No.	Pin Name	I/O	Description			
37	DOUT	О	Digital OUT output pin. (EIAJ format)			
38	FSX	О	Output pin for the 7.35kHz synchronization signal divided from the crystal osillator.			
39	EFLG	О	C1 C2 error correction monitor pin. Test input pin. Must be connected to OV.			
40	TEST	I	CT C2 that to the continue in the continue to			
41	ЕМРН	I/O	Emphasis pin. Which becomes an input pin after reset and can becontrolled externally This becomes an emphasis monitor pin under control by command.			
42	MUTEL	0	L channel mute output pin.			
43	MUTER	0	R channel mute output pin.			
73	WOILK		L channel power supply pin. (2000pF or more path controller to be inserted at a point			
44	LVDD		nearer to the pin between this pin and GND)			
45	LCHO	О	L channel output pin.			
46	LVSS		L channel ground pin, Must be connected to 0V.			
47	RVSS	-	R channel ground pin, Must be connected to 0V.			
48	RCHO	О	R channel output pin.			
40	DVDD		R channel power supply pin. (2000pF or more path controller to be inserted at a po			
49	RVDD		nearer to the pin between this pin and GND)			
50	VVDD		Crystal oscillator power supply pin. (2000pF or more path controller to be inserted			
50	XVDD	_	point nearer to the pin between this pin and GND)			
51	XIN	I	Comparing for 17.0244MHz and 1 collections			
52	XOUT	О	Connections for a 16.9344MHz crystal oscillator pin.			
53	XVSS	_	Crystal oscillator ground pin. Must be connected to 0V.			
54	ASLRCK	I	L/R clock input pin. (Must be connected to 0V when unused)			
55	ASDACK	I	Bit clock input pin. (Must be connected to 0V when unused)			
56	ASDFIN	I	L/R channel data input pin. (Must be connected to 0V when unused)			
57	LRSY	О	L/R clock output pin.			
58	DATACK	О	Bit clock output pin.			
59	DATA	О	L/R channel data output pin.			
60	16M	О	16.9344MHz output pin.			
	ana		Subcode frame synchronization signal output pin. This signal falls when the subcode			
61	SFSY	О	in the standby state.			
62	SBSY	О	Subcode clock synchronization signal output pin.			
63	PW	О	Subcode P, Q, R, S, T, U and W output pin.			
64	SBCK	I	Subcode readout clock input pin.			
65	CE	I	Chip enable signal input pin.			
66	CL	I	Data transfer clock input pin.			
67	DI	I	Data input pin.			
68	DO	О	Data output pin.			
69	*INT	О	Interruption signal output pin.			
70	*WRQ	0	Interruption signal output pin.			
71	*RES	I	Reset input pin. This pin must be set low briefly after power is first applied.			
72	DRF	0	Focus ON detect pin.			

Pin No.	Pin Name	I/O	Description	
73	VDD5V	_	Microprocessor interface power supply. (2000pF or more path controller to be inserted at a point nearer to the pin between this pin and GND)	
74	VSS		, , , , , , , , , , , , , , , , , , ,	
74	V 3 3	_	Digital ground pin. Must be connected to 0V.	
75	CONT6	I/O	General-purpose input/output pin 6.	
76	CONT7	I/O	General-pirpose input/output pin 7.	
77	V/*P	О	Rough servo/phase control automatic switching monitor output pin. "H" for rough servo and "L" for phase servo.	
78	FSEQ	О	Synchronization signal detection output pin. Outputs a high level when the synchronization signal detected from the EFM signal and the internally generated synchronization signal agree.	
79	DEFECT	I/O	Defect pin. Which becomes an input pin after reset and can be controlled externally. This becomes the defect monitor pin under control by command.	
80	EFMO	О	EFM signal output pin.	

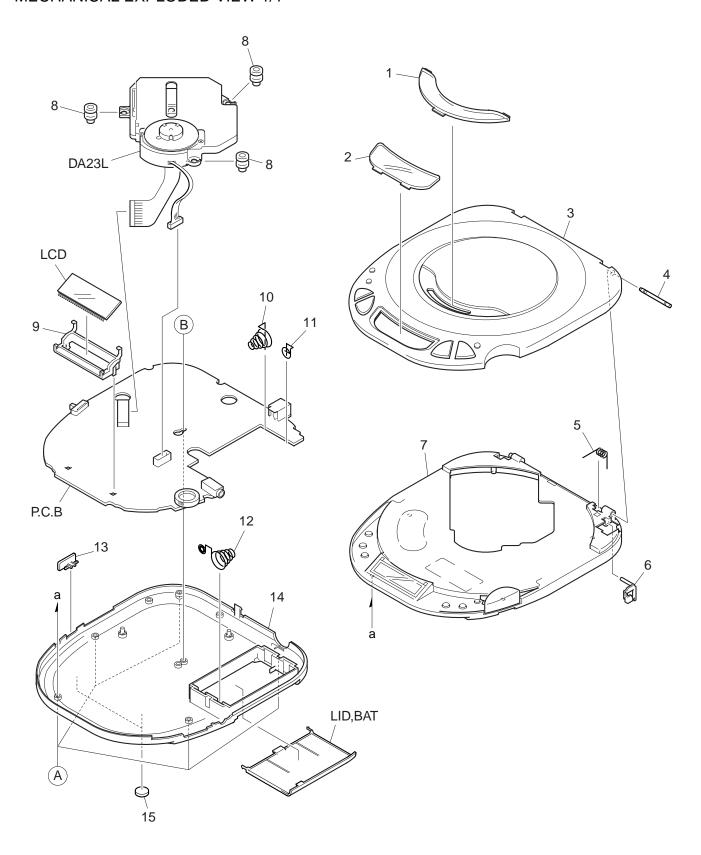
IC, µPD789405AGC-014-8BT

Pin No.	Pin Name	I/O	Description
1	VDO1	_	Positive polarity power supply (except for port section).
2	BIAS	_	Feeding the LCD drive power supply voltage.
3-5	VLC0-VLC2	_	LCD drive power supply voltage.
6	VSS1	_	Ground potential (except for port section).
7-10	COM0-COM3	О	Common signal output from LCD controller/driver.
11-38	S0-S27	О	Segment signal output from LCD controller/driver.
39	AVDD	_	A/D comparator analog power supply.
40	AVREF	_	A/D comparator reference voltage.
41-47	ANI6-0	I	Analog input signal to A/D comparator.
48	AVSS	_	A/D comparator ground potential.
40.52	INTERO INTERO	_	External interrupt input whose effective edge (rise-up or fall-down or both edges of
49-52	INTP3-INTP0	I	rise-up and fall-down) can be specified.
53	TO2	О	Output signal from 8-bit timer (TM02).
54	SI	I	Serial data input signal of serial interface.
55	so	О	Serial data output signal of serial interface
56	SCK	I/O	Serial clock input/output signal of serial interface
			Port 5.
			4-bit N-channel open-drain input/output port.
57-60	P53-P50	I/O	Input or output; can be specified in units of 1 bit.
			When it is used as an input port, built-in pull-up resistor can be used as specified by
			mask option.
			Port0.
			4-bit input/output port.
61-64	P03-P00	I/O	Input or output; can be specified in units of 1 bit.
			When it is used as an input port, built-in pull-up resistor can be used as specified by
			software
			Port 4.
CF	D47 D46	1/0	8-bit input/output port.
65, 66	P47, P46	I/O	Input or output; can be specified in units of 1 bit.
			When it is used as an input port, built-in pull-up resistor can be used as specified by
	DEGET	T	software.
67	RESET	I	System reset input.
68	X2		Terminal to connect external crystal for main system clock oscillation.
69	X1	I	
70	VSS0	_	Ground potential of port section.
71	VDD0	_	Positive polarity power supply for port section.
72	XT2		Terminal to connect external crystal for sub system clock oscillation.
73	XT1	I	
74	IC/VPP	-	This pin is internally connected. Connect this pin directly to Vss ₀ or Vss ₁ .
75-80	P45-P40	I	Key-return signal detection input signal.

IC, LA9253M

Pin No.	Pin Name	I/O	Description
1	FIN1	I	
2	FIN2	I	Pick-up signal input.
3	TIN1	I	rick-up signai input.
4	TIN2	I	
5	REF1	I	Pin designed for reference voltage.
6	VREF	О	Reference voltage output.
7	LDS	I	APC monitor voltage input.
8	LDD	О	APC output.
9	GND	_	GND.
10	LDOF	I	laser OFF pin (H: ON L: OFF).
11	ODRV	I	Speed switch pin (H: double L: normal speed).
12	AGON	I	AGC ON pin (H: ON L: OFF).
13	EFBL	I	FE balance adjustment pin.
14	TESO	О	TE signal output for TES.
15	TESI	I	TE input for TES formation.
16	TES	О	TES output.
17	HFL	О	HFL signal output.
18	TE	О	TE signal output.
19	TE-	I	Minus input for TE gain design.
20	FE	О	FE signal output.
21	FE-	I	Minus input for FE gain design.
22	RFEV	О	RF envelop signal output.
23	N/C	_	Pin N/C.
24	ВН	I	Capasitance connection pin for RF bottom clamp.
25	PH	I	Capasitance connection pin for RF gain design.
26	N/C	_	Pin N/C.
27	RF	О	RF signal output.
28	RF-	I	Minus input for RF signal gain design.
29	RFSW	I	Switch for equalizer design when RF has double speed.
30	VCC	_	Power supply.

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MECHANICAL PARTS LIST 1/1

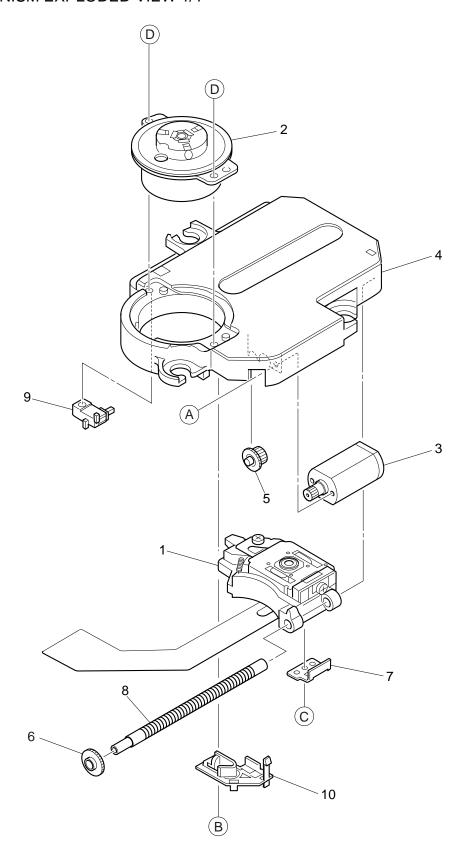
DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO		Kanri I No.	DESCRIPTION	REF. NO	PART NO.	KANRI NO.	DESCRIPTION
1	8A-HC7-007-010	WINDOW,CD		7	8A-HC7-066-0	10 CABI	ASSY, CENTER (D)
		<excep< td=""><td>T 1AHRJ1D,1AEZ1L,0Y1BLT></td><td></td><td></td><td></td><td><1AHRJ1D,0Y1BLT></td></excep<>	T 1AHRJ1D,1AEZ1L,0Y1BLT>				<1AHRJ1D,0Y1BLT>
1	8A-HC7-078-010	WINDOW, CD	(D)<1AHRJ1D,0Y1BLT>	7	8A-HC7-065-0	10 CABI	ASSY, CENTER (L) < 1AEZ1L>
1	8A-HC7-079-010	WINDOW, CD	(L)<1AEZ1L>	7	8A-HC7-035-0	10 CABI	ASSY, CENTER (LL) < 0 Y1BLL>
2	8A-HC7-005-010	WINDOW, DI	SPLAY	7	8A-HC7-067-0	10 CABI	ASSY, CENTER (LT) < 0Y1BS>
		<0Y1BLL,0	Y1S,1AEZ1S,0Y1BLT,0Y1BS>	8	8Z-HC1-225-0	10 DMPR	,MECHA(SP)
2	8A-HC7-019-010	WINDOW, DI	SPLAY 311				
		<1AHRJ1S,1AHR	J1D,1AEZ1L,1AHABS,0Y1BD>	9	8A-HC7-201-0	10 GUID	E,LCD
				10	8A-HC7-207-0	10 BAT-	CONTACT, (-) (HK)
2	8A-HC7-020-010	WINDOW, DI	SPLAY 312<2KS,2AEZ1S>	11	8A-HC7-206-0	10 BAT-	CONTACT, (+) (HK)
3	8A-HC7-017-010	LID ASSY,	CD	12	87-HC8-205-0	10 BAT-	CONTACT, (+)(-)
	<excep< td=""><td>T 1AHRJ1D,0Y1</td><td>BLL, 1AEZ1L, 0Y1BLT, 0Y1BS></td><td>13</td><td>8A-HC7-012-0</td><td>10 KNOB</td><td>,SL HOLD</td></excep<>	T 1AHRJ1D,0Y1	BLL, 1AEZ1L, 0Y1BLT, 0Y1BS>	13	8A-HC7-012-0	10 KNOB	,SL HOLD
3	8A-HC7-051-010	LID ASSY,	CD 310 (D)				
			<1AHRJ1D,0Y1BLT>	14	8A-HC7-016-0	10 CABI	ASSY, BOTTOM
3	8A-HC7-052-010	LID ASSY,	CD 310 (L)<1AEZ1L>		<exc< td=""><td>CEPT 1AHRJ</td><td>1D,0Y1BLL,1AEZ1L,0Y1BLT,0Y1BS></td></exc<>	CEPT 1AHRJ	1D,0Y1BLL,1AEZ1L,0Y1BLT,0Y1BS>
3	8A-HC7-053-010	LID ASSY,	CD 310 (LT)<0Y1BS>	14	8A-HC7-036-0	10 CABI	ASSY, BOTTOM (LL)
							<1AHRJ1D,0Y1BLL,1AEZ1L,0Y1BLT>
3	8A-HC7-037-010	LID ASSY,	CD 311 (LL)<0Y1BLL>	14	8A-HC7-075-0	10 CABI	ASSY, BOTTOM (LT) < 0Y1BS>
4	85-HC6-205-110	SHAFT, LID	(300) HK	15	88-HC6-021-0	10 FOOT	, DIA10
5	8A-HC7-204-010	SPR-T,OPE	1	A	87-067-869-0	10 V+1.	7-8 HL BLK
6	8A-HC7-018-110	LEVER, OPE	1				
7	8A-HC7-015-010	CABI ASSY	, CENTER	В	87-067-868-0	10 V+1.	7-4 HL BLK
	<excep< td=""><td>T 1AHRJ1D,0Y1</td><td>BLL, 1AEZ1L, 0Y1BLT, 0Y1BS></td><td></td><td></td><td></td><td></td></excep<>	T 1AHRJ1D,0Y1	BLL, 1AEZ1L, 0Y1BLT, 0Y1BS>				

COLOR NAME TABLE

Basic color symbol	Color	Basic color symbol	Color	Basic color symbol	Color
В	Black	С	Cream	D	Orange
G	Green	Н	Gray	L	Blue
LT	Transparent Blue	N	Gold	Р	Pink
R	Red	S	Silver	ST	Titan Silver
Т	Brown	V	Violet	W	White
WT	Transparent White	Y	Yellow	YT	Transparent Yellow
LM	Metallic Blue	LL	Light Blue	GT	Transparent Green
LD	Dark Blue	DT	Transparent Orange		

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CD MECHANISM PARTS LIST 1/1

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO	PART NO.	KANRI NO.	DESCR	RIPTION
1	S0-A41-A20-60	O PICK	UP LASER A	ASSY
2	SM-10A-108-00	1 MOTO	R ASSY SPI	INDLE
3	S0-M10-A10-90	0 MOTO	R SLED ASS	SY
4	S2-311-A12-20	0 CHAS	SIS	
5	S2-511-A23-20	0 GEAR	MIDDLE	
-	S2-511-A23-10 S2-511-A23-40 S2-511-A07-90 S4-S13-A00-20 S2-451-A18-10	0 GEAR 0 SPIN 0 SW,L	,SCREW ,RACK DLE SCREW EAF ER GEAR	
A B C D	SS-EXE-A04-00 SS-GXE-A00-30 SS-EXE-A14-10 SS-GXE-A00-20	0 SPEC	PAN PCS 1. IAL SCREW IAL SCREW IAL SCREW	

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ACCESSORIES/PACKAGE LIST

DESCRIPTIONで判断できない物は "REFERENCE NAME LIST" を参照してください。 If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

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REF. N	0	PART NO.	Kanri No.	DESCRIPTION
	1	8A-HC7-935-010		Z(EGF)C 311 F<1AEZ1L,0Y1BD>
	1	8A-HC7-955-010		Z(EGF)C 312 F<2KS,2AEZ1S>
	1	8A-HC7-937-010	IB,EZ	Z(PHNCZ)C 311 F<1AEZ1L,0Y1BD>
	1	8A-HC7-957-010	IB,EZ	Z(PHNCZ)C 312 F<2AEZ1S>
	1	8A-HC7-936-010	IB,EZ	Z(SID)C 311 F<1AEZ1L,0Y1BD>
	1	8A-HC7-956-010	IB,EZ	Z(SID)C 312 F<2AEZ1S>
	1	8A-HC7-902-010	IB,HF	R(ECA)C<1AHRJ1D>
	1	8A-HC7-912-010	IB,HF	R(ECA)C F<1AHRJ1S,1AHRJ1D>
	1	8A-HC7-914-010	IB,LH	H(3L)C F<1AHABS>
	1	8A-HC7-945-010	IB,Y((EGF)C 310 F<0Y1BLL,0Y1S,1AEZ1S,0Y1BLT,0Y1BS>
	1	8A-HC7-947-010	IB,Y((PHNCZ)C 310 F<0Y1BLL,0Y1S,1AEZ1S,0Y1BLT,0Y1BS>
	1	8A-HC7-946-010	IB,Y((SID)C 310 F<0Y1BLL,0Y1S,1AEZ1S,0Y1BLT,0Y1BS>
	2	87-B30-259-010	HEADI	PHONE, HP-M032(T) L<0Y1BS>
		87-B30-326-010	HEADI	PHONE, HP-M048 <except 0y1bs=""></except>
\triangle	3	87-B30-283-010	AC AI	DAPTOR, AC-D603ENC<2AEZ1S, 1AEZ1L, 0Y1BD>
 Λ	3	87-B30-287-010	AC AI	DAPTOR, AC-D603HANC<1AHABS>
A	3	87-B30-285-110	AC AI	DAPTOR, AC-D603HRNC<1AHRJ1S, 1AHRJ1D>
Λ	3	87-B30-284-010	AC AI	DAPTOR, AC-D603KNC<2KS>
Λ		87-A91-017-010	PLUG,	CONVERSION JT-0476<1AHRJ1S,1AHRJ1D>
	5	87-B30-141-010	BAT, N	NB-301 NC(2PCS)<2KS,2AEZ1S>

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